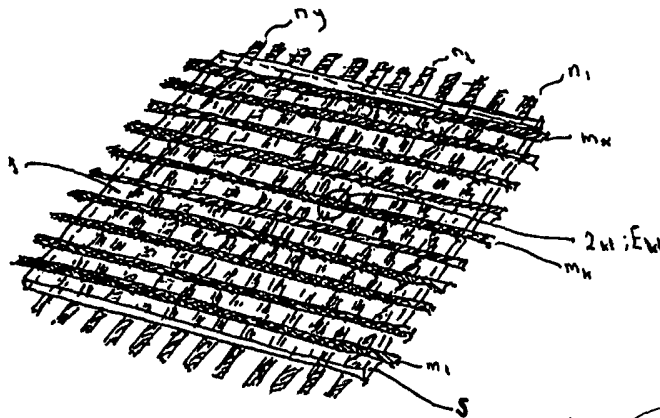




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : G11C 11/42, 13/02, H03K 19/12, 19/14		A2	(11) International Publication Number: WO 98/58383
			(43) International Publication Date: 23 December 1998 (23.12.98)
(21) International Application Number: PCT/NO98/00185 (22) International Filing Date: 17 June 1998 (17.06.98) (30) Priority Data: 972803 17 June 1997 (17.06.97) NO (71) Applicant (for all designated States except US): OPTICOM ASA [NO/NO]; Brynsveien 3B, N-0667 Oslo (NO). (72) Inventors; and (75) Inventors/Applicants (for US only): GUDESEN, Hans, Gude [NO/NO]; Tyrihansveien 5, N-1639 Gamle Fredrikstad (NO). LEISTAD, Geirr, I. [NO/NO]; Jongssubben 19, N-1300 Sandvika (NO). NORDAL, Per-Erik [NO/NO]; Båstadryggen 19, N-1370 Asker (NO).		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG). Published In English translation (filed in Norwegian). Without international search report and to be republished upon receipt of that report.	

(54) Title: ELECTRICALLY ADDRESSABLE PASSIVE DEVICE, METHOD FOR ELECTRICAL ADDRESSING OF THE SAME AND USES OF THE DEVICE AND THE METHOD



(57) Abstract

An electrically addressable passive device for registration, storage and/or processing of data comprises a functional medium (1) in the form of a continuous or patterned structure (S) which may undergo a physical or chemical change of state. The functional medium (1) comprises individually addressable cells (2) which represent a registered or detected value or are assigned a predetermined logical value for the cell. The cell (2) is provided between the anode (3) and cathode (4) in an electrode means (E) which contacts the function medium in the cell and causes an electrical coupling therethrough, the functional medium having a non-linear impedance characteristic, whereby the cell (2) directly can be supplied with energy which effects a change in the state of the cell. In a method for electrical addressing of the passive device wherein the addressing comprises operations for i.a. detection and registration as well as further operations for writing, reading and switching of a logical value assigned to the cell, electric energy is applied directly to the functional medium of the cell in order to change its state and hence effect an addressing operation. Use in optical detector means, volumetric data storage devices or data processing devices.

Chemical
Change
of state

Energy

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

Electrically addressable passive device, method for electrical addressing of the same and uses of the device and the method.

The invention concerns an electrically addressable passive device for registration, storage and/or processing of data, wherein the device comprises a functional medium in the form of a substantially layer-like continuous or patterned structure, wherein the functional medium can undergo a physical or chemical change of state by a suitable energetic influence, wherein the functional medium comprises individually addressable passive cells provided in a two-dimensional pattern, and wherein a given physical or chemical state in a cell represent a registered or detected value or are assigned a predetermined logic value for the cell, and a method for electrical addressing of a passive device for registration, storing and/or processing of data, wherein the device comprises a functional medium in the form of a substantially layer-like continuous or patterned structure, wherein the functional medium can undergo a physical or chemical change of state by a suitable energetic influence, wherein the functional medium comprises individually addressable cells provided in a two-dimensional pattern, wherein a given physical or chemical state in a cell represent a registered or detected value or are assigned a predetermined logic value for the cell, wherein the addressing comprises operations for detection of a registered or detected value in the cell and additional operations for writing, reading, erasing and switching of a logical value assigned to the cell, and wherein the method comprises supplying electrical energy directly to the functional medium of the cell for detecting or changing the physical and/or chemical state of the cell and hence cause an addressing operation.

The invention also concerns uses of the electrically addressable passive device and the method for its electrical addressing.

Particularly the invention concerns a logic device which may be used for data memories of the ROM type, the WORM type or for realising a data memory which may be erased and written once more, and a method for addressing of such memories by purely electronic means.

Even more particularly the invention concerns addressing of data memories wherein the memory medium substantially consists of organic materials and the addressing takes place over a passive matrix of electrical conductors which contacts the memory medium directly or indirectly.

Electronic addressing or logic devices, for instance for storage or processing of data are at present synonymous with inorganic solid state technology, and particularly crystalline silicon devices. Even if such devices have shown themselves technically and commercially very successful, they are encumbered with a number of disadvantages. Particularly they are encumbered with a complex architecture which leads to high cost and a loss of data storage density. Within the large subgroup of volatile semiconductor memories based on inorganic semiconductor material, the circuitry must constantly be supplied with electric current with a resulting heating and high electric power consumption in order to maintain the stored information. Non-volatile semiconductor devices on the other hand avoid this problem with a resulting reduction in the data rate accompanied by high power consumption and large degree of complexity. A number of different architectures has been implemented for memory chips based on semiconductor material and reflects a tendency to specialization with regard to different tasks. Matrix addressing of memory locations in a plane is a simple and effective way of achieving a great number of accessible memory locations with a reasonable number of lines for electrical addressing. In a square grid with n lines in each direction the number of memory locations hence scales as n^2 . In one form or another this is the basic principle which at present is implemented in a number of solid state semiconductor memories. In these cases however, each memory location must have a dedicated electronic circuit which communicates to the outside via the grid intersection point as well as a volatile or non-volatile memory element, typically a charge storage unit.

There has in the art been proposed a number of devices which are meant to realize addressable passive memory elements based on the use of an organic memory medium. Thus there is from JP-A-4-145664 (Takeda, assigned to Canon Inc.) known an organic electronic element wherein a thin film is provided between overlying and underlying electrodes, the underlying electrode being provided on a suitable substrate and the overlying electrode crossing the underlying electrode perpendicularly. By varying the electrical voltage between the electrodes the conductivity of the organic thin film is affected. This conductivity may be maintained permanently and used for representing a memory state in the thin film between a pair of electrodes.

There is, however, given no indication how this method and device may be used for addressing in large passive matrices.

JP-A-62-95883 (Yamamoto, assigned to Canon Inc.) discloses a memory cell with a first underlying electrode formed by depositing copper on a glass substrate and over the electrode depositing a thin film of a charge-transfer organometallic complex, in this case Cu-TCNQ, whereafter an overlying
5 electrode formed by depositing an aluminium paste on the thin film. If the electric potential of the first electrode is higher than that of the second electrode, the thin film is kept in a high resistance state until the electric field reaches a threshold intensity and is thereafter switched to a low resistance state. No indication is here given that such memory elements straightforwardly
10 may be provided in large passive matrices. Generally it is, however, well known to form a memory device wherein the memory medium is a bistable switchable thin film in the form of an organic complex compound of the charge-transfer type, cf. also JP-A-62-95883 with the same inventor and applicant, wherein transistor switches are used in each memory element for
15 addressing.

In JP-A-3-137896 (Taomoto, assigned to Matsushita Giken K.K.) there is once more proposed a memory element which uses an organic thin film which may be switchable bistably between a high resistance state and a low resistance state by application of an electric field and maintains the momentary resistance state
20 after the electric field has been withdrawn. Further this element may change the state very fast at high temperature, but slower at low temperature. Again an organic thin film is located between an overlying and an underlying electrode and provided on a substrate. It is stated that the switching takes place faster and faster with increasing temperature, but nothing is said of the use of a
25 memory element of this kind in large passive matrices and whether it is suitable for passive matrix addressing. Further there is from JP-A-3-137894 (Asakawa, assigned to Matsushita Giken K.K.) known to provide thin films between overlying and underlying electrode matrices. In the actual case the matrix is shown as a 6·11 matrix, hence with a total of 66 elements. The thin
30 film is a vapour deposited phthalocyanine film. If a voltage higher than a threshold value is applied to an electrode intersection, an on state is stored. When a voltage equal to the threshold value is applied, the intersection point is irradiated with light, such that the on state is stored in this part and information supplied in the form of light may be written directly into the
35 matrix. When a reversed voltage is applied to the intersection point, the on-state is erased. Hence a structure which realizes a memory function both with an electric signal and a light signal is obtained. Even if there here are used a

6-11 matrix, it is in no way evident that this bistable switchable memory element will function without error when addressing in a passive matrix with a large number of memory elements.

- 5 Finally there is in a paper by Z. Y. Hua & G.R. Chen, "A new material for optical, electrical and electronic thin film memories", Vacuum 43, No. 11, pp.1019-1023 (1992), described a new category of erasable memory media which allows the realisation of memory elements which may be switched bistably by supplying energy in the form of heat, electrical fields or light radiation under different conditions. These memory media are based on the
- 10 above-mentioned organometallic charge-transfer complex M(TCNQ) formed in 7,7,8,8-tetracyanoquinodimethane ($C_{12}H_4N_4$) which acts as an electron acceptor molecules with different metals (M) as electron-rich donors. Hua & Chen propose the use of M(TCNQ) in an electrically erasable memory by for instance forming a matrix of switching elements based on Cu(TCNQ) between
- 15 a set of underlying electrodes, for instance of aluminium, and a set of overlying intersecting parallel electrodes, for instance of copper, which is oriented perpendicularly in relation to the underlying electrodes. The authors are aware of the sneak current problem when forming memory devices based on passive matrix addressing of this kind and hence in order to avoid erroneous
- 20 read-out propose to add a layer of material between the Cu(TCNQ) film and the underlying electrode for forming a schottky barrier. Thus the problem with sneak currents is substantially eliminated and the use of M(TCNQ) in combination with the schottky barrier will hence be able to realize addressing of memory elements in large passive matrices. It is thus realized that in order
- 25 to avoid the sneak current problem by addressing in large passive matrices of memory elements for storing of data, it is necessary to take the materials engineering conditions in regard. This is particularly important when one in addition to a pure memory function wishes to realize switching, registration or detecting functions in the matrix and wherein the current and voltage values
- 30 may vary widely, such that a diode function is not always a necessary condition. It may also be the desired to combine the electrical addressing in a passive matrix with light emitting or light detecting devices, which make further demands on the material used, particularly when it is desired to realize passive matrices with for instance 10^8 elements or cells per cm^2 .
- 35 Generally it has turned out difficult to address bistable or multistable switchable memory media in passive matrices and the problems with both

addressability and reliable detection only increases with increasing numbers of nodes in the matrix, such as comprehensive simulation tests conducted by the applicant have shown. The same tests have also established that these problems may be surmounted by use of suitable materials with special electric or electronic properties.

The first main object of the present invention is thus to provide an electrically addressable passive device which avoids the problems with the known semiconductor devices for storage of data and which makes possible addressing of a very large number of cells, for instance about 10^8 in one cm^2 for registration, storage and processing of data in an all-electronic format and without comporting disadvantages such as complexity, high cost, high power consumption and volatile storage.

Further there is also an object of the present invention that the electrically addressable passive device shall be realized with a functional medium in the form of an organic material which offers possibilities for flexible technical solutions and a low cost far below that for corresponding devices based on inorganic crystalline semiconductors.

A second main object of the present invention is to provide a method for electrical addressing of passive device according to the invention and particularly it is the object that the method shall be realized as an addressing of a passive matrix in a wholly electronic format, wherein the functional medium in the passive device is formed by substantially organic materials and implements a very large number of cells, for instance about 10^8 on one cm^2 , for registration, storage and/or processing of data.

Finally there is also an object of the present invention to employ the electrical addressable passive device as well as a method for addressing in a passive matrix in order to implement an optical detector means or in order to implement a volumetrically organised addressable device for storage and/or processing of data.

The above-mentioned objects and advantages are achieved according to the invention with an electrical addressable passive device which is characterized in that the cell is provided between the anode and the cathode of an electrode means which directly or indirectly contacts the functional medium in the cell in order to effect a direct or indirect electrical coupling through the cell, whereby the cell directly can be supplied with electrical energy for detection

of the physical or chemical state of the cell or a change in the physical or chemical state of the cell, that a plurality of substantially parallel electric conductors are provided on each side of the functional medium such that overlying and underlying conductors intersect each other in a substantially orthogonal mutual relationship, that the electrode means of the cell is formed in an intersection point between a respective overlying and underlying conductor such that the cells in the functional medium and their assigned electrode means form the elements of a matrix whose rows and columns are defined by respectively the overlying and underlying electric conductors which in the intersection points form the anodes and the cathodes of the electrode means, and that the functional medium of the cell has a non-linear impedance characteristic, said functional medium being formed as a homogenous or stratified structure comprising one or more substantially organic materials and with a method which according to the invention is characterized by providing the functional medium of the cell with a non-linear impedance characteristic, said functional medium being formed with a homogenous or stratified structure comprising one or more substantially organic materials, supplying electrical energy to the cell by providing the cell between the anode and the cathode in an electrode means which directly or indirectly contacts the substantially organic material in the cell, and applying to the cell an electric voltage which effects a direct or indirect electrical coupling through the cell.

According to the invention the electrically addressable passive device and the method according to the invention may be employed in an optical detector means and in a volumetric data storage device or data processing device.

According to the invention the cell preferably comprises a rectifying diode formed between the anode and the cathode, such that the device comprises an electrical network of such diodes.

It is according to the invention also preferable that the electric conductors are provided in or on the functional medium and contact this directly or that on each side of the functional medium and between this and the electric conductors a dielectric layer is provided such that the electric conductors contact the functional medium indirectly. Further it is preferable that the electric conductors are provided in or on a substantially layer-like substrate adjacent to the functional medium on each side thereof.

In order to realize for instance an optical detector means the electric conductor is in advantageous manner at least on one side of the functional medium formed of a transparent material.

5 In a preferred embodiment the organic material comprises a polymer material, the polymer material preferably being a conjugated polymer.

10 Preferably an organic material is an anisotropic electrical conducting material or has been added a substance which may emit light by application of electric energy, such that this organic material under influence of the emitted light and possibly heat generated by the electric energy applied can undergo a chemical reaction which effects a change in the impedance of the functional medium.

In a preferred embodiment an organic material has been added one or more substances which by application of electric energy may emit or detect light in different wavelengths or in different wavelength bands or been added fusible microcrystallites, preferably on or in the surface of this organic material.

15 In another preferred embodiment an organic material is a ferroelectric liquid crystal or a ferroelectric polymer. In a further preferred embodiment an organic material itself or a substance added to this organic material can transfer from a glass phase to an amorphous phase or vice versa or an organic material may be a multistable conformation reactive organic material.

20 In a preferred embodiment of the method according to the invention the cell is formed in the intersection point between respective substantially parallel electric conductors provided on each side of the functional medium such that an overlying and an underlying conductor intersects each other in a substantially orthogonal mutual relationship and comprise respectively the
25 anode and the cathode of an electrode means for the cell, such that the cells in the functional medium and their assigned electrode means form elements in a matrix whose rows and columns are defined by respectively the overlying and underlying electric conductors.

30 Preferably electric energy is supplied by injection of electric charges in the cell.

Further in the method according to the invention it is preferred that the cell is formed with a highly non-linear voltage characteristic.

Preferably the method in the cell is formed with a rectifying diode.

Wherein an organic material has been added a substance which can emit light by application of electric energy such that the emitted light, possibly together with heat generated by the electric energy application, triggers a chemical reaction in this organic material, the impedance of the functional medium can
5 be changed in an advantageous manner.

Wherein an organic material has been added one or more substances which by application of electric energy can emit light on different wavelengths or in different wavelength bands, it is preferred that spectral characteristics of the emitted light is changed by varying the voltage value of the applied electric
10 energy. It is in this connection also advantageous that a logical value assigned to the cell is switched by applying a high voltage value to the cell and reading the logical value by detection of the light emission during an applied low voltage value, such that the cell emits long wavelength light which does not influence the physical or chemical state of the functional medium, the intensity
15 of the long wavelength light being dependent on the logical value.

Wherein an organic material is a ferroelectric crystal or a ferroelectric polymer, it is preferred that a logical value assigned to the cell is detected by measuring the impedance of the cell.

It is also preferred that a logical value assigned to the cell is switched by the
20 supplied electric energy changing the electrical conductivity of the functional medium by resistance heating.

Further it is also preferred that the logical value assigned to the cell is switched irreversibly by effecting an irreversible change in the electric conductivity of the functional medium or that the logical value is switched
25 irreversibly by effecting an irreversible change in the interface between the functional medium and the anode respectively the cathode.

Wherein an organic material has been added fusible microcrystallites, it is preferred that the logical value assigned to the cell is switched irreversibly by effecting a fusing of the microcrystallites.

30 It may also be preferable that wherein an organic material itself or a substance added to this organic material can transfer from a glass phase to an amorphous phase a logical value assigned to the cell is switched irreversibly by effecting a transition between the glass phase and the amorphous phase or vice versa in said organic material or the substance added thereto.

Wherein an organic material is a multistable conformation reactive organic material, it is preferable that a logical value assigned to the cell is switched irreversibly by generating an electrical field in the cell.

5 The invention shall now be discussed in more detail in connection with examples of embodiments of the electrically addressable passive device and the method according to the invention with reference to the appended drawings wherein

- fig. 1a schematically and in perspective shows a preferred embodiment of a matrix addressable passive device according to the invention,
- 10 fig. 1b an equivalent diagram of the device in fig. 1a realized as a diode network,
- fig. 2 schematically an embodiment of a cell in the device in fig. 1,
- fig. 3 another embodiment of a cell in the device in fig. 1,
- fig. 4 the device according to the invention used in a volumetric data storage device or data processing device,
- 15 fig. 5 schematically the problem with so-called sneak currents or alternative current paths in an matrix addressable passive device according to the invention,
- fig. 6 an example of addressing with generation of light emission in a cell,
- 20 fig. 7 an example of addressing using organic light emitting diodes in a cell, and
- fig. 8 an example of addressing using a ferroelectric liquid crystalline material in the cell.

25 Fig. 1 shows the electrically addressable passive device according to the invention realized as a matrix addressable device. The functional medium 1 is provided in the form of a planar layer S. The functional medium is an organic material with non-linear impedance characteristic and possible with different substances added in order to realize desired detecting or switching functions. On the upper surface of layer S there are provided lines for electrical

30 addressing in the form of a plurality of electric conductors m, and on the lower surface of the layer S there are correspondingly provided lines for electrical addressing in the form of parallel electric conductors n, the conductors m, n

being provided mutually orthogonal such that they form a matrix. In fig. 1 is the device shown with x conductors m and y conductors n such that the conductors form a planar orthogonal x, y matrix. A logic cell 2 in the device is generated in the volume between two intersecting electric conductors m, n .

- 5 This is emphasized in fig. 1a by the formation of an element 2_{kl} in the intersection point between the k 'th conductor m and the l 'th conductor n . - If all elements 2 are formed with a rectifying function, the device may be represented by an electric network of rectifying diodes, such as shown by the equivalent diagram in fig. 1b.
- 10 The opposing portions of the conductor m_k and n_l in an intersection point comprises together the electrode means E_{kl} of the logic cell 2_{kl} , as the anode 3 in the electrode means may be the conductor m_k and the cathode 4 in the electrode means the conductor n_l . - For several of the purposes of the present invention the cell 2_{kl} may as well be denoted as a logic element or logic cell, as
- 15 the material or the functional medium in the cell by the addressing may assume different physical or chemical states which may represent electrically detectable logical values.

In section this is shown schematically in fig. 2, wherein the functional medium 1 is provided in the form of a layer between the conductors m_k, n_l , the anode 3 and the cathode 4 of the electrode means E_{kl} being comprised by respectively

20 the relevant portion of the conductor m_k and the relevant portion of the conductor n_l in the intersection point between the conductors. In this intersection point, i.e. between the anodes 3 and the cathode 4, a passive logic cell is formed, in fig. 2 denoted as 2_{kl} in order to indicate that it is located

25 between the conductor m_k and the conductor n_l . Fig. 2 is, of course, only a section of fig. 1 and it is to be understood that the whole section taken along the conductor m_k will show a total of y logic cells 2 and y conductors n_y . If $x \neq y$, the device, of course, forms a rectangular area with $x \cdot y$ logic cells and if $x = y$, the device is square with x^2 cells.

- 30 A more complex embodiment of the device according to the invention, exemplified as a section through a cell 2 is shown in fig. 3. Here the electric conductor m is provided on a substrate 5, while the electric conductor n correspondingly is provided on a substrate 6. As shown in fig. 2 the conductors m, n might contact the functional medium 1 directly, but in the embodiment in
- 35 fig. 3 it is further shown provided dielectric layers 7; 8 between respectively the conductors $m; n$ or the substrates 5; 6. Hence the electrode means E by its

anode 3 and its cathode 4 no longer contacts the functional medium 1 directly, but indirectly via the dielectric layer 7, 8, such that an indirect electric coupling is formed through the cell 2. This coupling could for instance be inductive or capacitive. If the dielectric layers 7, 8 are absent, the electrode means E will, of course, contact the functional medium 1 directly and a corresponding direct or ohmic coupling is obtained through the cell 2.

Briefly stated the volume between the anode 3 and the cathode 4 of the electrode means E, the scale of which roughly is defined by the widths of the conductors m, n and the distance therebetween, i.e. the thickness of the functional medium 1, defines a logic cell 2 which for instance forms a detector element in an optical detector or a memory element in a data storage device or a switching element in a data processing device.

The anode 3 and the cathode 4 which surrounds the functional medium 1 are included in the electrode means E which, when an electric voltage is applied thereto, will cause a physical or chemical change of state in the functional medium. This will cause a change in the electric impedance between the anode 3 and the cathode 4 and this impedance change can be detected on the electric conductors m, n which form the electrode means E in question. The logical state or the logical value in each intersection point between m and n or in each cell 2 may then be determined by measuring the electric impedance between the electric conductors m, n which form the electrode means E of the cell 2.

It shall here be remarked that there is an essential difference between the prior art matrix addressable logic devices, for instance data storage devices, and the present invention, as the latter employs a functional medium with non-linear impedance characteristic and formed by one or more organic materials and this has far-reaching implications in regard of constructural flexibility, operational features and costs. An important feature of using a functional medium of this kind is the possibility of an extensive use of purely passive addressing even in very large matrices, e.g. from 10^6 to 10^8 elements provident with a density of for instance 10^8 elements per cm^2 , as there will not be required any discrete active circuit elements in the intersection points.

The device according to the invention forms a substantially plane layer-like structure S and this implies that it will be possible to stack such plane layer-like structures S layerwise and form a volumetric logic device, for instance a volumetric memory device. This may for instance be realized as in fig. 4,

wherein there is shown a volumetric device of this kind consisting of stacked layers of structures S_1, \dots, S_z shown in section through a row of the cells 2 of the device, one of which is indicated in the figure. The embodiment of the logic device according to the invention and the method used for electrical addressing in a matrix-based format at the same time realize proximity addressing, i.e. the signals for addressing are conveyed in immediate connection to the functional medium 1 and influences this over the electrode means E which in fig. 4 is shown for a logic cell 2 with respectively the anode 3 and the cathode 4 in the structure S_1 . If several structures S_1, \dots, S_z is stacked upon each other, they must be mutually isolated, preferably by an isolating layer 9 which may be electrically, thermally or optically isolating.

In principle each cell 2 of the device may have a very small extension, for instance of the magnitude of some ten nanometers and even less if the functional medium 1 for instance is based on layers of e.g. polymer materials. The thickness of the structure S becomes correspondingly small and hence it will be seen that the device according to the invention with the use of electrical addressing of the cell in proximity shall render it possible to implement a volumetric data storage device with very large capacity both with regard to storage density and transfer rates. The device according to the present invention has a close analogy in principally correspondingly implemented optical data storage devices based on proximity addressing and realized in volumetric embodiment. Such optical data storage devices are moreover disclosed and discussed in international patent application PCT/NO97/00154 which belongs to the present applicant and which hereby is included by reference, such that no further details shall be discussed in connection with the embodiment in fig. 4 and the use of the device and the method according to the invention in volumetrically implemented data storage or data processing devices, but reference only made to what has been disclosed in the above-mentioned patent application.

An important reason that the passive solid state memories have not been implemented in large scale, for instance with use of fusible resistances, is due to the problem with alternate current paths or so-called sneak currents in the network of electric conductors. This problem is schematically shown in fig.5, wherein the electric conductors once more is denoted by m, n and a logic cell 2_{kl} is shown formed in the intersection point between the conductor m_k and the conductor n_l . If the logic cell 2 in each intersection point consists of an ohmic resistance such this is indicated in fig. 5, this implies that the resistance change

in given location x, y in the current conductor matrix due to the addressing operation will be masked by currents which are leaking through alternative loops as this is shown in fig. 5, wherein the logic cell 2_{kl} in the position $x = k$ and $y = l$ shall be addressed, while current leaks along the stitched indicated
5 current paths to neighbouring cells. The correct current path for the addressing on respectively conductors m and n is indicated with a broad continuous line. It is seen that the problem with sneak currents only are amplified if the size of the current conductor matrix increases, i.e. with the magnitude of the product xy . Two ways of avoiding the problem shall be discussed below, namely the
10 use of rectifying diodes or materials with high impedance, for instance liquid crystals or a conformation reactive material.

In order to avoid the problem with sneak currents this may for instance take place by giving the electric connection in the intersection point, i.e. the cell, a highly non-linear current/voltage characteristic, something which will reduce
15 or eliminate the sneak current problem. As will be evident from an inspection of fig. 5, this may be achieved by providing a rectifying diode in series with the resistance in each intersection point. It is consequently of essential importance for the present invention to form a network of such rectifying diode in a simple and reliable manner which comports low cost and at the same time is able to
20 generate suitable data carrying structures in the form of adjustable and non-linear impedances. By for instance using a functional medium with an organic material in the form of a conjugated polymer of the thiophene or PPV type and with a suitable choice of electrode material in relation to the organic material, it is possible to generate a diode junction at the interface between metal and
25 organic material and this diode can be given very good rectifying properties. The electric coupling through the functional medium of the cell is controlled by the impedance properties of the latter. Addressing of the logic cell hence will imply a change in the impedance between the conductors m and n at an intersection point, for instance as shown in fig. 5 between m_k and n_l and this
30 may be achieved in a number of ways.

Below there shall now be given examples of preferred embodiments of the method for electrical addressing according to the present invention and primarily the examples will be directed towards writing and possibly reading of data in a logic cell.

35 Example 1 – Writing by Joule heating

A high current which is sent through a logic cell provided at an intersection

point x, y between electric conductors m, n will heat a functional medium based on organic conducting material. By judicious choice of material its bulk impedance can be changed reversibly or irreversibly by a current pulse and the changed impedance be read by measurement on the conductors m, n which form the electrode means E of the logic cell.

If the device according to the invention is implemented as a data storage device with high storage density, the logic cells will be very close together and it becomes important to avoid cross-talk in the form of thermal diffusion which causes an impedance change in neighbouring cells during a write operation.

Hence it is mandatory to apply short write pulses, as the typical spatial definition which may be achieved can be expressed by the thermal diffusion length. The latter is a well-defined quantity which depends on bulk parameters as well as the geometry and the time characteristics of the heating pulse. Typically it will be necessary with pulse widths below 1 microsecond in order to achieve a spatial solution less than one micrometer.

Another form of cross-talk which is particularly relevant in reading of data is current spread between logic cells in a functional medium formed of organic bulk material. This may be avoided by using an organic material with a strongly anisotropic electric conductance, i.e. a high conductance through the cell between the anode 3 and the cathode 4, while the conductance in the extension direction of the structure S, i.e. along the layer or the plane formed by S and from memory cell to memory cell is low. - Polymer compounds with properties of this kind is by the way disclosed by M. Granström, "Macromolecular microstructures", Linköping Studies in Science and Technology, Dissertation No. 432, Linköping 1996, particularly pp. 49-51 and pp. 134-158; cf. M. Granström & al.: "Self organising polymer films – a route to novel electronic devices based on conjugated polymers", published in Supramolecular Science. - Ideally the impedance properties of the cells shall not be changed during read operations. This implies that thermally driven processes must be highly non-linear or threshold-dependent, i.e., that the functional medium shall not be influenced at low to moderate currents, but that there shall be a well-defined and steep transition at higher currents (non-linear impedance characteristic). Crystalline materials typically show thermally driven changes at a well-defined melting point. In amorphous materials as such as polymer and glass such transitions, however, takes place gradually over an extended temperature range and it is common to employ the less precise

parameter softening temperature or glass transition temperature (the Vicat point).

5 There is ample experimental documentation that the life-time of polymer-based diodes strongly depends on the operating conditions, as the main cause which leads to failure is Joule heating. Even with a poorly defined thermal transition temperature, such diodes however, very well satisfy the requirements which are given above in regard of distinguishing between the write and the read operation.

10 Melting point driven thresholding function may be achieved in polymers and other diode materials by incorporating microcrystallites in the conducting organic bulk material or by partly covering the contact surface between electrodes and the organic material therewith. One way of implementing the latter measure is electrode spraying of crystallites on the electrode surfaces before the device is mounted. The effects of the microcrystallites may consist
15 in a mechanical rupture of the current path due to melting, for instance good wetting properties in relation to the electrode material in order to promote diffusion in the lateral direction, or release of active substances which inhibit the current propagation.

Example 2 – Writing by light generation

20 It is well-known that exposure to light may precipitate or accelerate chemical modifications in organic materials.

In one embodiment of the device according to the present invention the organic bulk material wholly or partly is formed of a light emitting organic compound which is activated by applying an electric current. Organic light emitting
25 diodes (OLEDs) of this kind is at present under commercial development. Degradation mechanisms in OLEDs has been the subject of extensive investigations and an important result is shielding against the irradiation of the diode bulk material with ultraviolet or blue light, something which is essential in regard of achieving long lifetimes.

30 In the present invention the susceptibility of ultraviolet and blue light to generate an intense and controlled local irradiation by excitation of OLEDs which emit ultraviolet and/or blue light is exploited. As disclosed below, the effect of the irradiation which typically is accompanied by OLED Joule heating may be exploited in two different ways. The first of these is basing
35 readout on detecting a change in impedance in the logic cell, the second of

these is basing readout on detecting a change in the light emitting properties of OLED.

In order to minimize power consumption and increasing the addressing speed, materials which are strongly susceptible to photo-induced state changes are chosen. This implies an optimisation which is exactly the opposite of what is usual in the manufacturing of OLEDs as well as the use of some interesting materials which formerly were rejected in connection with the development of OLEDs. In the present case there is, however, a very large degree of freedom with regard to choice of materials. It is hence possible to use interacting material systems which includes a light-sensitive photo initiator which is custom-made for specific stimulation of the light emitting component in OLED. Such measures may be directed towards both the functional medium in the logic cell and the material at the electrode surfaces.

Example 3 – Writing by internal light generation and reading by direct impedance measurement

In this case the only function of the light is to initiate and/or accelerate the chemical modification of the functional medium 1 of the cell 2. A simple generic structure is shown in fig. 6, wherein a polymer which emits ultraviolet and/or blue light is provided between the anode 3 and the cathode 4 of the electrode means and creates a rectifying junction at one of the interfaces. Biasing in the forward direction causes current flow thereto, light is emitted and interacts with the organic material of the cell 2. Simultaneously heat is generated and the relation between supplied electric energy dissipated as heat and used for generation of light, depends on the functional medium and the electrical excitation conditions. The combined effect of heat and light causes chemical changes in the cell by one or several pertinent mechanisms. One is chain separation in conjugated molecules, something which reduces the conductivity of the bulk material and changes the series resistance in each cell. Another mechanism is indirect, as the added chemical species becomes chemically reactive by the effect of light and attack the electrical conducting materials in the cell. One instance of the latter is the generation of free radicals by ultraviolet and/or blue light.

Example 4 – Writing and reading by internal light generation

A generic structure is shown in fig. 7. Herein each memory cell comprises a mixture of microscopic organic light emitting diodes (OLEDs) 10; 10' which respectively emits light in two or more wavelengths. OLEDs 10, 10' are in fig.

7 shown as elliptical domains which extend between and contacts the anode 3 and the cathode 4. For instance domain 10 may then emit ultraviolet or blue light, while domain 10' may emit red light. Cells consisting of several OLEDs and domains may be embodied as shown in the paper "White light emission
5 from a polymer blend light emitting diode" by M. Granström and O. Inganäs, Applied Phys. Lett. 68:2, pp. 147-149, 1996, cf. the above-mentioned dissertation by Granström, pp. 135-140, as the present invention may be based on OLEDs which emit in blue and red and which are provided randomly in the volume of the cell. Excitation with low voltage excites only OLEDs which
10 emit red light, while a higher voltage also excites OLEDs which emit blue light.

Writing takes place by excitation at a high voltage such that the emission of blue light and increased heat dissipation reduce or destroy the light emission of OLEDs which emit red light. Once again the photosensitive additives may be
15 applied as discussed in connection with example 3 above. Reading takes place by excitation at low voltage, such that only OLEDs which emit red light are activated and the heat generation becomes low. The irradiated power of the cell in question is monitored by a photodetector and the logic state of the cell can hence be determined.

20 Example 5 – Capacitive logic cells based on liquid crystals

The sneak current problem which was mentioned above will not be present in passive matrix addressable logic devices with very high impedance at each intersection point, i.e. in the location of each single logic cell. A device with an extremely high ohmic resistance is shown in fig. 8. A logic cell 2 contains
25 bistable liquid crystal material (LC) provided between the anode 3 and the cathode 4 of the electrode means E. The logic state of the cell 2 is represented by the degree of molecular order in the LC, something which may be controlled by applying voltage to the electrode. The basic principle of this may be explained as follows. Suppose that the logic cell 2 on the left side of fig. 8
30 is in a disordered state and represents a logical value 0. Application of polarizing voltage for writing aligns the molecules of the liquid crystal and results in an ordered state which represent the logical value 1, shown by the logic cell 2 to the right in fig. 8. Removing the voltage leaves the molecules in LC in the aligned ordered state and a non-volatile logic state is obtained.
35 Thereafter the cell may return to the logic state 0 by applying a voltage pulse or a sequence of voltage pulses with a value which causes erasing. According to the present invention the logical value of the logic cell 2 is determined by

detecting the difference in electrical impedance between the logic states 0 and 1 in the cell. As a concrete example the capacitance of the logic cell 2 may approximately be given as $C = C_0 + C_1$, where C_0 is a contribution which weakly depends on the aligned state of the LC molecules, while C_1 is a contribution which explicitly is related to molecular alignment.

Reading of data involves determination of the degree of ordering of the LC molecules. $M_{LC} \approx f(C_1)$ is a direct measure of the orientational mobility of the LC molecules and will be different in respectively the 0 and 1 states. Two different preferred schemes for electrical excitation with regard to a determination of M_{LC} shall be discussed. In order to visualise these in a simple manner the LC molecules are shown in fig. 8 as rods with a random orientation in disordered state in the logic cell 2 at left in the figure. In the logic cell 2 at right in the figure the LC molecules are aligned along the direction between the anode 3 and the cathode 4 and are in the ordered state. This does, however, not prevent that a more complex ordering may take place in logic cells based on a ferroelectric liquid crystal. In a first scheme a continuous alternating voltage (bipolar voltage) is applied to the electrode means and with a frequency which is sufficiently low to allow the LC molecules to react with a partial reorientation, i.e. $f \lesssim 1/\tau$, where τ is the time constant of small signal reorientation and f the frequency. If the molecules are relatively mobile, typical for a disordered state, a larger value will be detected for M_{LC} than in the ordered state, wherein the molecules are locked along their local ordering vector.

In the second scheme the voltage level is applied to the electrodes and with a read voltage which is lower than the voltage used for writing. Dependent on whether the LC molecules are in the 0 state or in the 1 state they will have different orientational mobility and the size of M_{LC} as derived from a measurement of the transient displacement current will contain information about the ordering of the LC molecules, hence the logic state of the logic cell. Both these schemes have their advantages and weaknesses. The scheme of applying continuous alternating voltage provides precision, but simultaneously leads to a reduction in the response time of the detection. The scheme with voltage steps gives detection in a time period which is $\lesssim \tau$, but represents a larger degree of technical complexity. In both cases a relatively high and threshold-defined write voltage simplifies the readout process. On the other

hand a high write voltage must be balanced against the desirability of using low voltage drive electronics and power sources.

Example 6 – Electrically switched bistable organic films

Complex organic molecules, e.g. of biological origin, may have bistability or multistability, i.e. they may appear in different conformations. A class of molecules of this kind is bacteriorhodopsin and variants thereof or related compounds. The conformational reactive properties of bacteriorhodopsin with regard to use in an optical logic device is moreover closely discussed in NO patent application No. 972574 which belongs to the present applicant and which hereby is incorporated by reference.

A bistable memory device may be obtained on basis of the matrix addressable embodiment of the logic device as mentioned above. A thin film of e.g. bacteriorhodopsin may then for instance be the organic material in the functional medium 1. The thin film is preferentially aligned and switched between two bistable molecular conformations under influence of the electric field generated between the anode 3 and the cathode 4 in the electrode means E when voltage is applied thereto. Switching may take place reversibly and be performed a large number of times without exhausting the memory material. Reading takes place by applying a voltage above the switching threshold to the electrode means. If the cell in question already has been switched in a preceding write operation, there will be no further response in the cell on applying the read voltage. On the other hand, if the cell has not been switched, it will now respond. The switching is accompanied by an electric transient which may be sensed via an electronic circuit connected over the electrode means which drives the cell in question.

In order to obtain the desired properties in an electrically addressable passive device according to the invention the organic material in the functional medium must have a non-linear impedance characteristic. In addition to the organic materials discussed above, it may for instance be topical to use a ferroelectric polymer material in order to form capacitive cells. Another topical material may be organic based charge-transfer complexes with non-linear impedance characteristics, for instance the above-mentioned M(TCNQ) complex. If the device according to the invention shall be realized as an optical detector this may take place by using a functional medium wherein the organic material forms light sensitive diodes, for instance polymers corresponding to

those which were used in the above-mentioned OLEDs. A light sensitive polymer with the desired spectral selectivity must then be used.

Realized as an optical detector it is to be understood that the device forms a detector array in a plane, e.g. any stacked configuration is not relevant. On the other hand the electrodes m,n in the electrode means E must at least on one side be formed from a transparent or translucent material, i.e. on the side of light incident. For instance the electrodes m or n may be of ITO (indium tin oxide) or a transparent or translucent polymer such as polypyrrol.

For a number of purposes there will in an optical detector of this kind be desired to be able to adjust the spectral selectivity, for instance with the intention of multispectral detection. This could take place by using an electrically tuneable optical filter over the transparent electrodes, for instance provided in the form of a continuous layer with a separate drive electronics. Another possibility would be the use of a light sensitive diode material whose spectral selectivity could be tuned electrically, such that tuning could take place directly over the electrode means E. At present this has turned out to be impossible in practice. A more closer alternative which is possible to accomplish in practice, would be to pattern the functional medium, i.e. not forming it as a continuous layer, but assigned separately to the singular cells. For instance could groups of three and three adjacent cells form a pixel in an RGB detector, with the cells in question in each pixel selective against red, green and blue light and addressed according to a protocol suitable for RGB detection.

The functional medium itself in the device according to the invention may be the deposited as a continuous layer between the electrodes in the electrode device. There is, however, nothing to prevent the functional medium being patterned, i.e. deposited under separate portions between the electrodes in each separate electrode means. In this case there will be no problem if the functional medium has an isotropic electrical conductivity. In addition the functional medium may be formed with homogenous structure, for instance comprising one organic material, possible with one or more additives. Such additives may themselves be inorganic or organic. However, there is nothing to prevent the formation of a functional medium with a stratified structure, such that the materials which comprises the functional medium is provided in layers between the electrodes in the electrode means. Each layer may then consist of different organic materials with the desired electric or electronic properties and

possibly with inorganic or organic additives. Also in this case one and possibly several layers may be formed by an inorganic material. It may for instance be relevant to apply a combination of amorphous hydrogenated silicon (a-Si:H) adjacent to a polymer material, such that a hybrid inorganic/organic functional medium is obtained. If an organic based charge-transfer complex, for instance an organometallic complex such as (M(TCNQ)) is used in the functional medium, the former must have a non-linear impedance characteristic, but can additionally be provided together with a semiconductor material provided adjacent to the electrodes on one or the other side of the functional medium, and which for instance may be gallium arsenide such that a schottky junction is obtained as per se known and discussed in the introduction. If it is desired to realize the functional medium with a diode function or a rectifying function, a stratified embodiment makes it possible to realize these functions in different technologies, for instance with use of PN, NPN, PNP or PIN junctions and possibly also in hybrid inorganic/organic embodiment. Stratified functional media also provide possibility of using light emitting or light detecting materials provided in separate layers if this is desired.

A person skilled in the art will realize that the logic device according to the present invention in a practical embodiment also will comprise drivers for the electrodes and wherein feed lines and current busses must be used in order to convey voltage to electric conductors m,n. The external supply of electrical energy to the logic device according to the invention may however be implemented in a number of ways which shall be well-known to persons skilled in the art and hence does not fall within the scope of the present invention and is consequently not discussed more closely in the above.

PATENT CLAIMS

1. Electrically addressable passive device for registration, storage and/or processing of data, wherein the device comprises a functional medium (1) in the form of a substantially layer-like continuous or patterned structure (S),
5 wherein the functional medium can undergo a physical or chemical change of state by a suitable energetic influence, wherein the functional medium (1) comprises individually addressable passive cells (2) provided in a two-dimensional pattern, and wherein a given physical or chemical state in a cell (2) represent a registered or detected value or are assigned a predetermined
10 logic value for the cell, characterized in that the cell (2) is provided between the anode (3) and the cathode (4) of an electrode means (E) which directly or indirectly contacts the functional medium (1) in the cell in order to effect a direct or indirect electrical coupling through the cell, whereby the cell (2) directly can be supplied with electrical energy for detection of the physical or
15 chemical state of the cell or a change in the physical or chemical state of the cell, that a plurality of substantially parallel electric conductors (m;n) are provided on each side of the functional medium (1) such that overlying and underlying conductors (m;n) intersect each other in a substantially orthogonal mutual relationship, that the electrode means (E) of the cell (2) is formed in
20 the intersection point between a respective overlying and underlying conductor (m;n) such that the cells in the functional medium (1) and their assigned electrode means (E) form the elements of a matrix whose rows and columns are defined by respectively the overlying and underlying electric conductors (m;n) which in the intersection points form the anodes (3) and the cathodes (4)
25 of the electrode means (E), and that the functional medium of the cell (2) has a non-linear impedance characteristic, said functional medium (1) being formed as a homogenous or stratified structure comprising one or more substantially organic materials.
2. Electrically addressable passive device according to claim 1,
30 characterized in that the cell (2) comprises a rectifying diode formed between the anode (3) and the cathode (4) of the electrode means (E), such that the device forms an electrical network of diodes of this kind.
3. Electrically addressable passive device according to claim 1,
characterized in that the electric conductors (m;n) are provided in or on the
35 functional medium (1) and contacts this directly.

4. Electrically addressable passive device according to claim 2,
characterized in that a dielectric layer (7;8) is provided on each side of the
functional medium (1) and between this and the electric conductors (m;n),
such that the electric conductors (m;n) contact the functional medium (1)
indirectly.
5. Electrically addressable passive device according to claim 3 or claim 4,
characterized in that the electric conductors (m;n) are provided in or on a
substantially layer-like substrate (5;6) adjacent to the functional medium (1)
on each side thereof.
6. Electrically addressable passive device according to claim 1,
characterized in that the electric conductors (m;n) on at least one side of the
functional medium (1) being formed by a transparent material.
7. Electrically addressable passive device according to claim 1,
characterized in that an organic material comprises a polymer material.
8. Electrically addressable passive device according to claim 7,
characterized in that the polymer material is a conjugated polymer.
9. Electrically addressable passive device according to claim 1,
characterized in that an organic material is an anisotropic electrical conducting
material.
10. Electrically addressable passive device according to claim 9,
characterized in that the anisotropic electrical conducting material includes
separate electrical conducting domains (10, 10') surrounded by an electrical
isolating material.
11. Electrically addressable passive device according to claim 10,
characterized in that the conducting domains (10, 10') are formed by phase
separation between at least two organic liquids which are distributed in the
form of a substantially layer-like structure before the electric conductors (m;n)
and possible substrates (5;6) for these are provided on each side of the
functional medium (1).
12. Electrically addressable passive device according to claim 1,
characterized in that a substance which can emit light by application of electric
energy has been added to an organic material, and that this organic material
under influence of the emitted light and possibly heat generated by electric

energy applied can undergo a chemical reaction which effects a change in the impedance of the functional medium.

13. Electrically addressable passive device according to claim 1,
characterized in that one or more substances have been added to an organic
5 material and are capable of emitting or detecting light on different wavelengths
or in different wavelength bands by application of electric energy.

14. Electrically addressable passive device according to claim 1,
characterized in that the functional material is a ferroelectric liquid crystal or a
ferroelectric polymer.

10 15. Electrically addressable passive device according to claim 1,
characterized in that fusible microcrystallites have been added to an organic
material, preferably on or in the surface of this organic material.

16. Electrically addressable passive device according to claim 1,
characterized in that an organic material itself or a substance added to this
15 organic material can transfer from a glass phase to an amorphous phase or vice
versa.

17. Electrically addressable passive device according to claim 1,
characterized in that an organic material is a multistable conformation reactive
organic material.

20 18. A method for electrical addressing of a passive device for registration,
storing and/or processing of data, wherein the device comprises a functional
medium (1) in the form of a substantially layer-like continuous or patterned
structure (S), wherein the functional medium (1) can undergo a physical or
chemical change of state by a suitable energetic influence, wherein the
25 functional medium (1) comprises individually addressable cells (2) provided in
a two-dimensional pattern, wherein a given physical or chemical state in a cell
(2) represent a registered or detected value or are assigned a predetermined
logic value for the cell, wherein the addressing comprises operations for
detection of a registered or detected value in the cell and additional operations
30 for writing, reading, erasing and switching of a logical value assigned to the
cell, and wherein the method comprises supplying electrical energy directly to
the functional medium of the cell for detecting or changing the physical and/or
chemical state of the cell and hence cause an addressing operation,
characterized by providing the functional medium (1) of the cell (2) with a

non-linear impedance characteristic, said functional medium (1) being formed with a homogenous or stratified structure comprising one or more substantially organic materials, supplying electrical energy to the cell (2) by providing the cell between the anode (3) and the cathode (4) in an electrode means (E) which
5 directly or indirectly contacts the substantially organic material in the cell and applying to the cell an electric voltage which effects a direct or indirect electrical coupling through the cell.

19. A method according to claim 18, characterized by forming the cell (2) in the intersection point between respective substantially parallel electric
10 conductors (m;n) provided on each side of the functional medium (1), an overlying and an underlying conductor (m;n) intersecting each other in a substantially ortogonal mutual relationship and comprising respectively the anode (3) and the cathode (4) in an electrode means (E) of the cell (2), such that the cells (2) in the functional medium (1) and their assigned electrode
15 means (E) form elements in a matrix whose rows and columns are defined by respectively the overlying and underlying electric conductors (m;n).

20. A method according to claim 19, characterized by forming the electric conductors on at least one side of the functional medium by a transparent material.

20 21. A method according to claim 18, characterized by electric energy being supplied by injection of electric charges in the cell.

22. A method according to claim 18, characterized by electric energy being supplied by generating electric fields in the cell.

23. A method according to claim 18, characterized by the cell being formed
25 with a highly non-linear voltage characteristic.

24. A method according to claim 18, characterized by the cell being formed with a rectifying diode.

25. A method according to claim 24, characterized by the rectifying diode being spontaneously formed by directly contacting an organic material,
30 preferably a conjugated polymer, between the anode and cathode.

26. A method according to claim 18, wherein a substance which can emit light by application of electric energy has been added to an organic material such that the emitted light, possibly together with heat generated by electric

energy application, triggers a chemical reaction in this organic material, characterized by changing the impedance of the functional medium.

27. A method according to claim 18, wherein one or more substances which by application of electric energy can emit or detect light on different
5 wavelengths or in different wavelength bands have been added to an organic material, characterized by the spectral characteristics of the emitted light being changed by varying the voltage value of the applied electric energy.

28. A method according to claim 27, characterized by a logical value assigned to the cell being switched by applying a high voltage value to the cell,
10 and reading the logical value by detection of the light emission during an applied low voltage value, such that the cell emits long wavelength light which does not influence the physical or chemical state of the functional medium, the intensity of the long wavelength light being dependent of the logical value.

29. A method according to claim 18, wherein an organic material is a
15 ferroelectric liquid crystal or a ferroelectric polymer, characterized by detecting a logical value assigned to the cell by measuring the impedance of the cell.

30. A method according to claim 18, characterized by a logical value assigned the cell being switched by the supplied electrical energy due to
20 resistance heating changing the electrical conductivity of the functional medium.

31. A method according to claim 18, characterized by a logical value assigned the cell being switched irreversibly by effecting an irreversible change in the electrical conductivity of the functional medium.

25 32. A method according to claim 18, characterized by a logical value assigned to the cell being switched irreversibly by effecting an irreversible change in the interface between the functional medium and the anode respectively the cathode.

30 33. A method according to claim 18, wherein fusible microcrystallites have been added to an organic material, characterized by a logical value assigned to the logic cell being switched irreversibly by effecting a fusing of the microcrystallites.

34. A method according to claim 18, wherein an organic material itself or a substance added to this organic memory material may transfer from a glass phase to an amorphous phase or vice versa, characterized by a logical value assigned to the cell being switched reversibly, the switching being caused by a transition between a glass phase and an amorphous phase or vice versa in the organic material or in the substance added thereto.
35. A method according to claim 18, wherein an organic material is a multistable conformation reactive organic material, characterized by a logical value assigned to the cell being switched reversibly by generating an electric field in the cell.
36. The use of a an electrically addressable passive device according to any of the claims 1-17 and a method for electrical addressing according to any of the claims 18-35 in an optical detector means.
37. The use of a an electrically addressable passive device according to any of the claims 1-17 and a method for electrical addressing according to any of the claims 18-35 in a volumetric data storage device or data processing device.

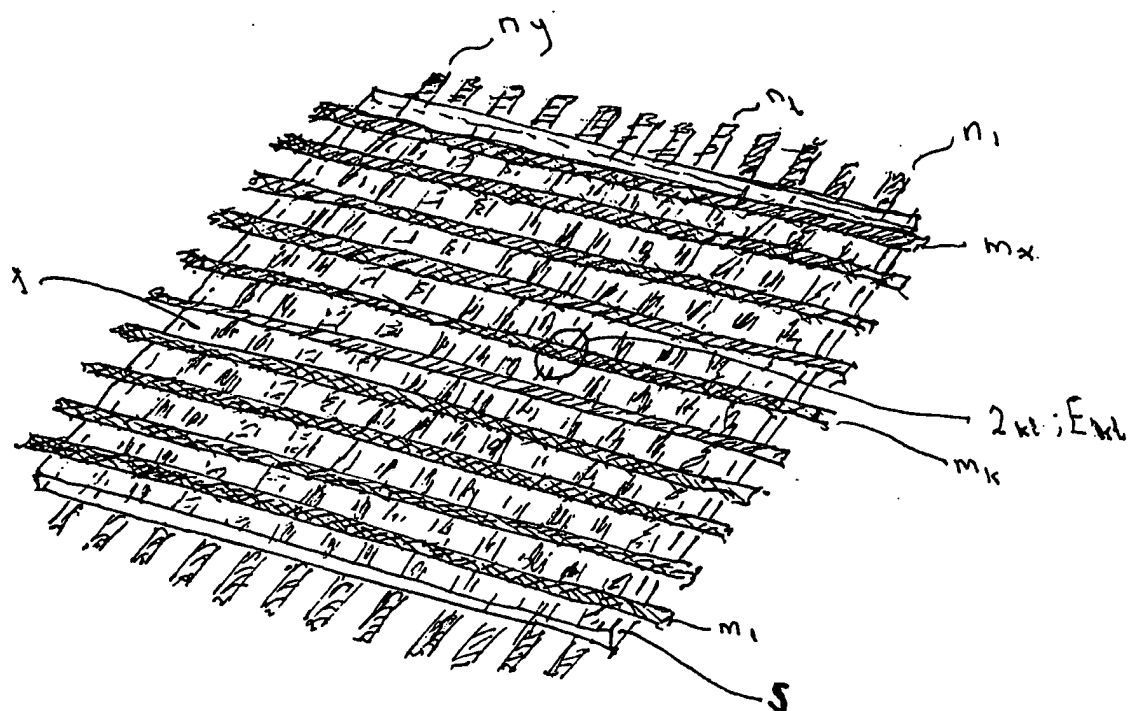


Fig. 1a

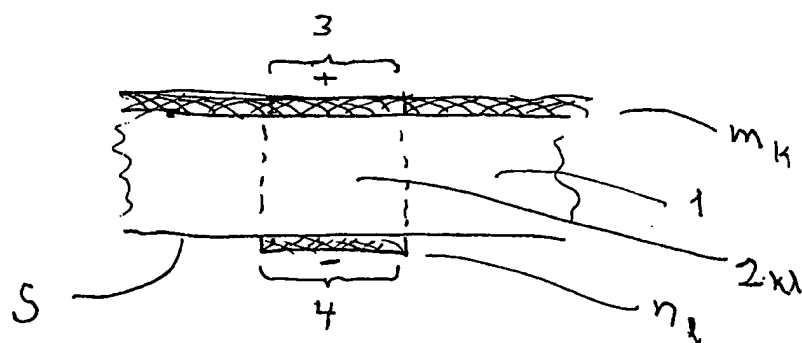


Fig 2

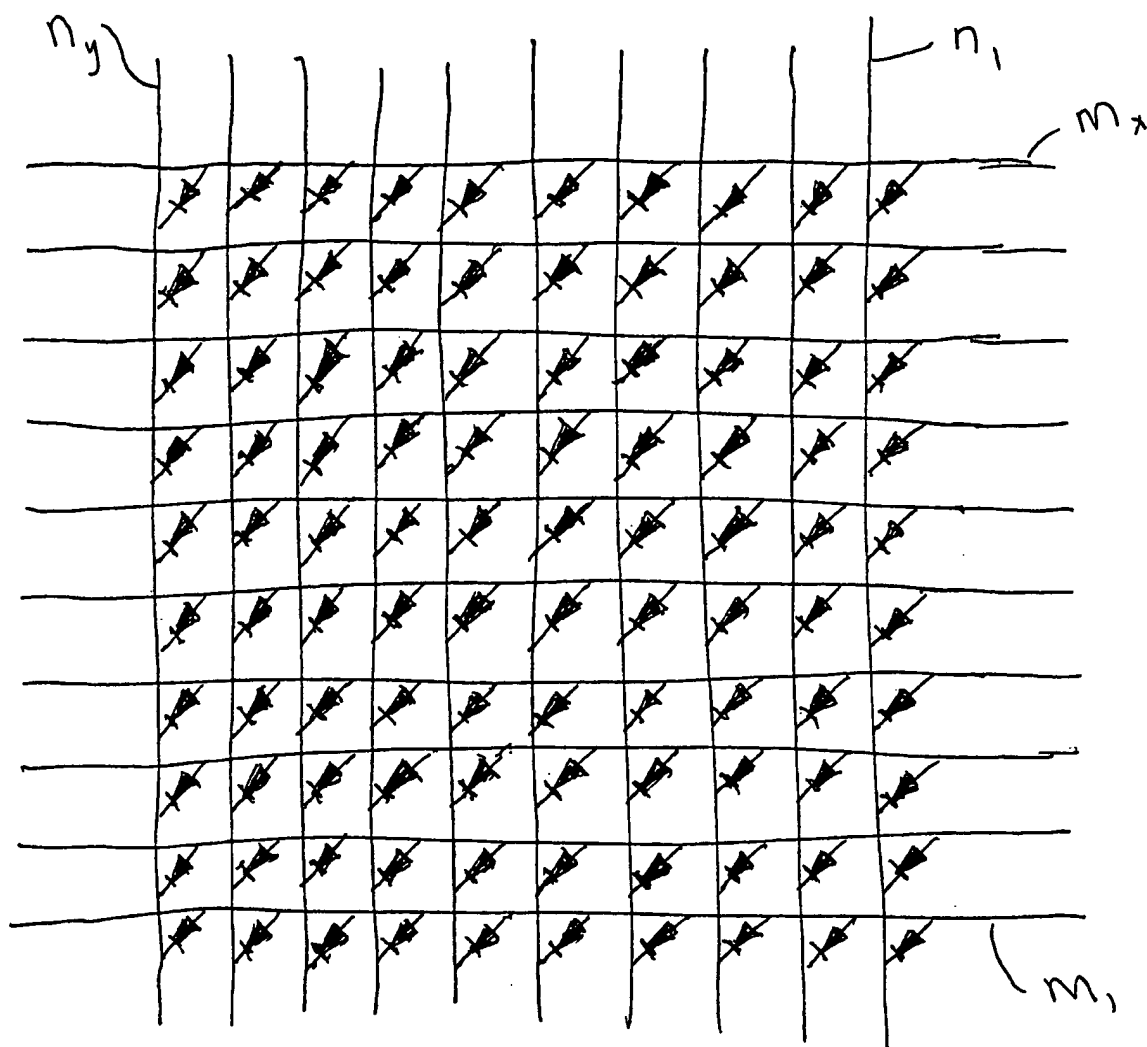


Fig. 1b

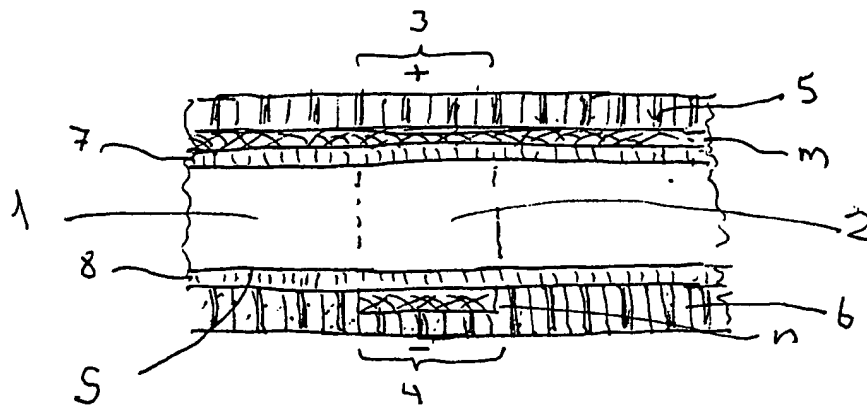


Fig. 3

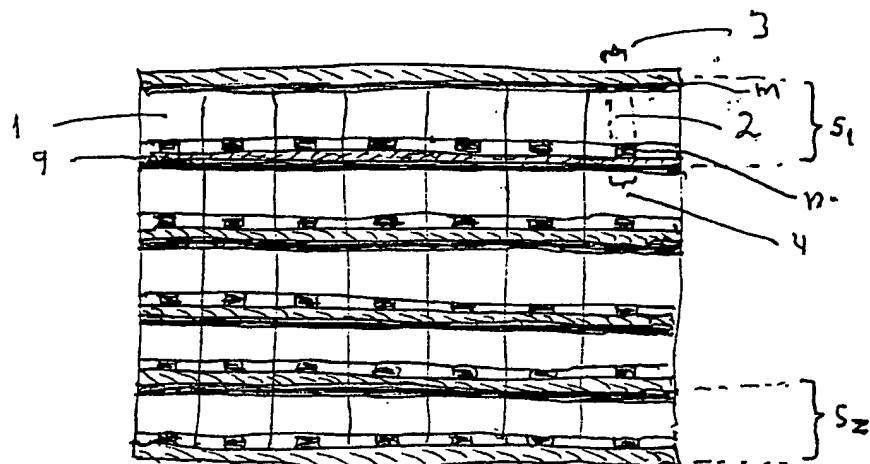


Fig. 4

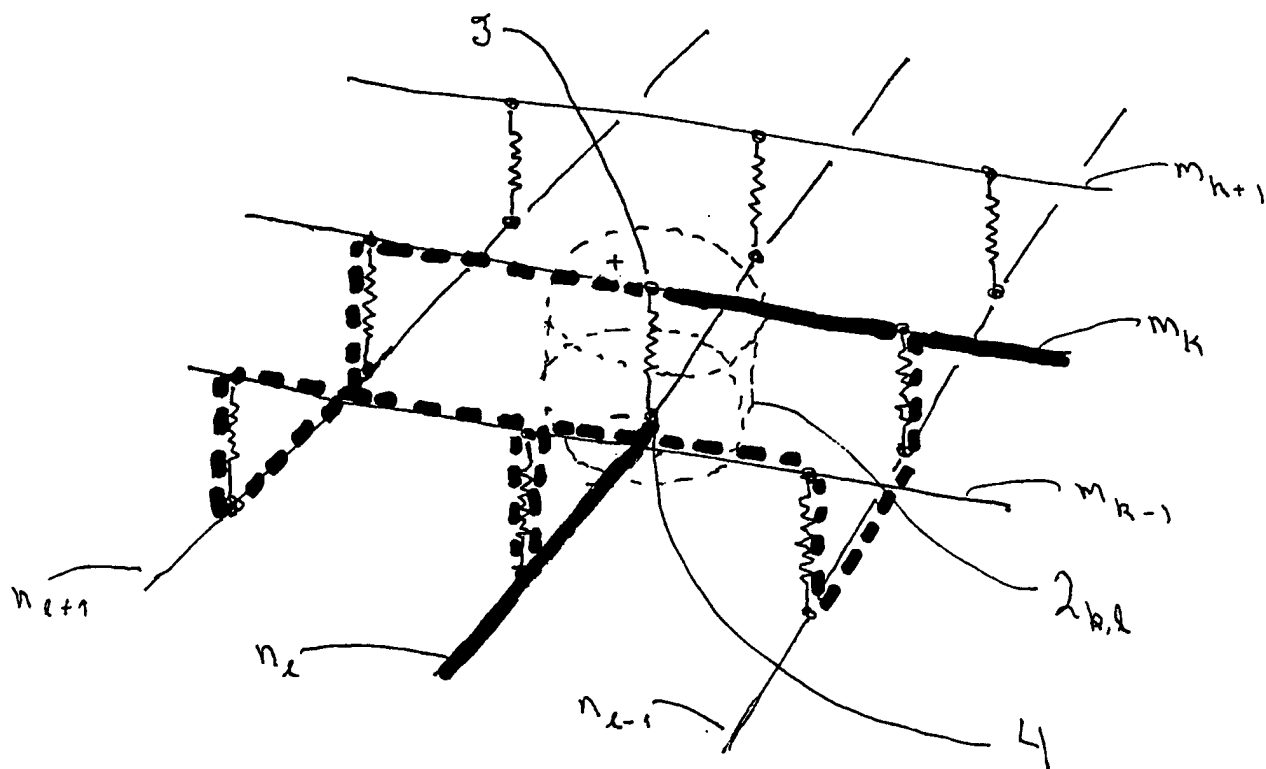


Fig. 5

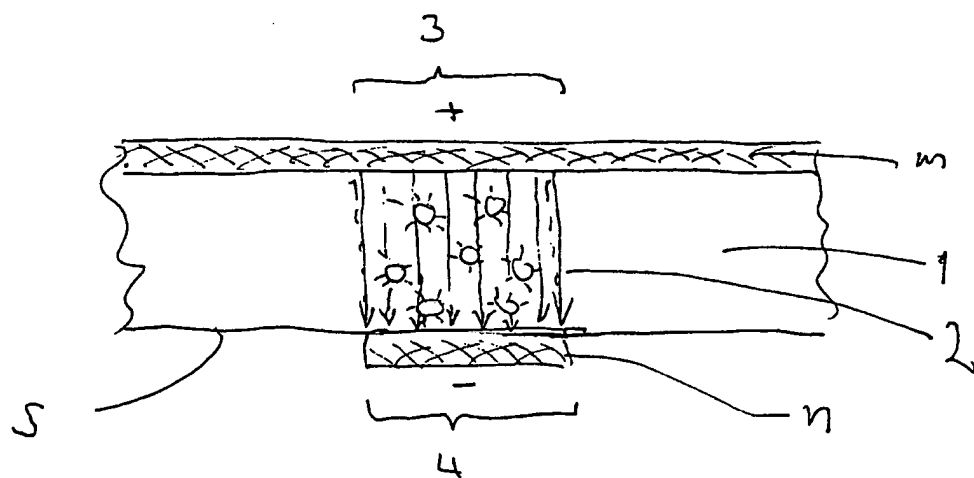


Fig. 6

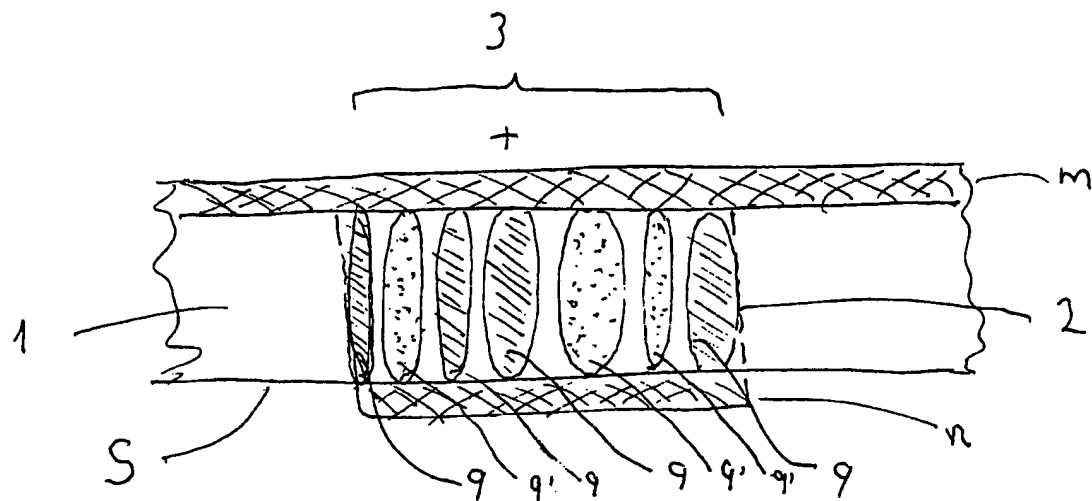


Fig. 7

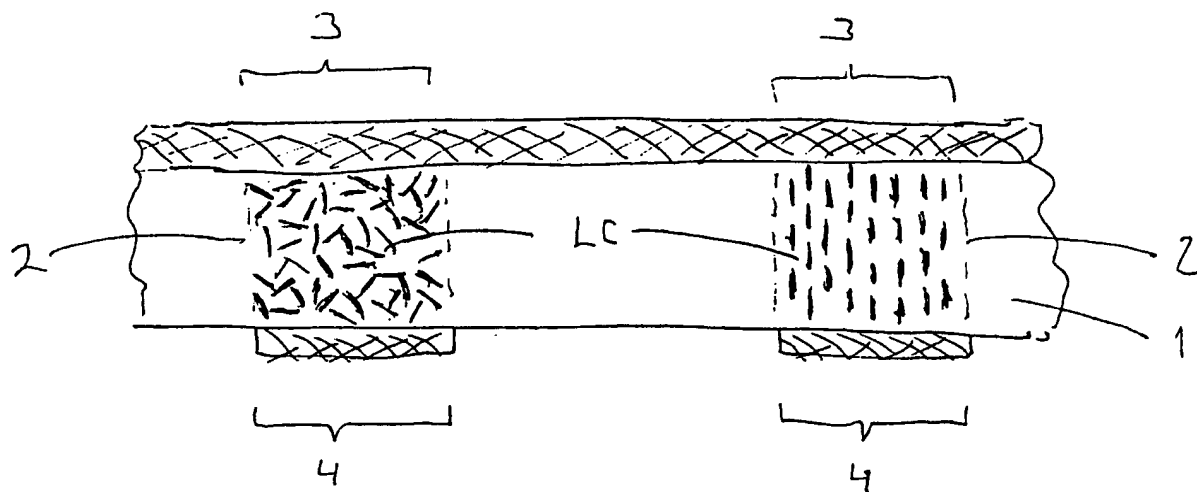


Fig. 8



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶: G11C 11/21, 11/22, 11/42, 13/02, H03K 19/177	A3	(11) International Publication Number: WO 98/58383 (43) International Publication Date: 23 December 1998 (23.12.98)
(21) International Application Number: PCT/NO98/00185 (22) International Filing Date: 17 June 1998 (17.06.98) (30) Priority Data: 972803 17 June 1997 (17.06.97) NO (71) Applicant (for all designated States except US): OPTICOM ASA [NO/NO]; Brynsveien 3B, N-0667 Oslo (NO). (72) Inventors; and (75) Inventors/Applicants (for US only): GUDESEN, Hans, Gude [NO/NO]; Tyrihansveien 5, N-1639 Gamle Fredrikstad (NO). LEISTAD, Geirr, I. [NO/NO]; Jongsstubben 19, N-1300 Sandvika (NO). NORDAL, Per-Erik [NO/NO]; Båstadryggen 19, N-1370 Asker (NO).	(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i> <i>In English translation (filed in Norwegian).</i> (88) Date of publication of the international search report: 1 April 1999 (01.04.99)	
(54) Title: ELECTRICALLY ADDRESSABLE PASSIVE DEVICE, METHOD FOR ELECTRICAL ADDRESSING OF THE SAME AND USES OF THE DEVICE AND THE METHOD		
(57) Abstract <p>An electrically addressable passive device for registration, storage and/or processing of data comprises a functional medium (1) in the form of a continuous or patterned structure (S) which may undergo a physical or chemical change of state. The functional medium (1) comprises individually addressable cells (2) which represent a registered or detected value or are assigned a predetermined logical value for the cell. The cell (2) is provided between the anode (3) and cathode (4) in an electrode means (E) which contacts the function medium in the cell and causes an electrical coupling therethrough, the functional medium having a non-linear impedance characteristic, whereby the cell (2) directly can be supplied with energy which effects a change in the state of the cell. In a method for electrical addressing of the passive device wherein the addressing comprises operations for i.a. detection and registration as well as further operations for writing, reading and switching of a logical value assigned to the cell, electric energy is applied directly to the functional medium of the cell in order to change its state and hence effect an addressing operation. Use in optical detector means, volumetric data storage devices or data processing devices.</p>		

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav	TM	Turkmenistan
BF	Burkina Faso	GR	Greece		Republic of Macedonia	TR	Turkey
BG	Bulgaria	HU	Hungary	ML	Mali	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MN	Mongolia	UA	Ukraine
BR	Brazil	IL	Israel	MR	Mauritania	UG	Uganda
BY	Belarus	IS	Iceland	MW	Malawi	US	United States of America
CA	Canada	IT	Italy	MX	Mexico	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NE	Niger	VN	Viet Nam
CG	Congo	KE	Kenya	NL	Netherlands	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NO	Norway	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's	NZ	New Zealand		
CM	Cameroon		Republic of Korea	PL	Poland		
CN	China	KR	Republic of Korea	PT	Portugal		
CU	Cuba	KZ	Kazakhstan	RO	Romania		
CZ	Czech Republic	LC	Saint Lucia	RU	Russian Federation		
DE	Germany	LI	Liechtenstein	SD	Sudan		
DK	Denmark	LK	Sri Lanka	SE	Sweden		
EE	Estonia	LR	Liberia	SG	Singapore		

INTERNATIONAL SEARCH REPORT

International application No.

PCT/NO 98/00185

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: G11C 11/21, G11C 11/22, G11C 11/42, G11C 13/02, H03K 19/177
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: G02F, G11C, H03K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EDOC, WPI, JAPIO, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	Vaccum, Volume 43, No 11, 1992, Z.Y. Hua et al, "A NEW MATERIAL FOR OPTICAL, ELECTRICAL AND ELECTRONIC THIN FILM MEMORIES", page 1019 - page 1023, cited in the document	1-3,7,14,18, 19,22,23,35, 37
Y		5,24
A		15,30
	--	
Y	Patent Abstracts of Japan, Vol 15, No 360, P-1250 abstract of JP 3-137894 A (MATSUSHITA GIKEN K.K.), 12 June 1991 (12.06.91), cited in the application	1-3,5,7,18, 19,22-24,35, 37
	--	

☒ Further documents are listed in the continuation of Box C.☒ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

20 January 1999

Date of mailing of the international search report

25 -01- 1999

Name and mailing address of the ISA/

Swedish Patent Office

Box 5055, S-102 42 STOCKHOLM

Facsimile No. +46 8 666 02 86

Authorized officer

Bo Gustavsson

Telephone No. +46 8 782 25 00

INTERNATIONAL SEARCH REPORT

International application No.

PCT/NO 98/00185

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	Patent Abstracts of Japan, Vol 15, No 360, P-1250 abstract of JP 3-137896 A (MATSUSHITA GIKEN K.K.), 12 June 1991 (12.06.91), cited in the application --	1-3,7,18,19, 22,23,35,37
Y	EP 0721189 A2 (NEC CORPORATION), 10 July 1996 (10.07.96), claim 1 --	14
A	EP 0395886 A2 (OLYMPUS OPTICAL CO., LTD.), 7 November 1990 (07.11.90), page 4, line 17 - line 47, abstract --	1,3,5-8, 18-23,37
A	US 5592409 A (KIYOSHI NISHIMURA ET AL), 7 January 1997 (07.01.97), see the whole document --	1,3-6,14,18, 19,22,29, 35-37
A	Patent Abstracts of Japan, Vol 16, No 423, E-1260 abstract of JP 4-145664 A (CANON INC.), 19 May 1992 (19.05.92), cited in the application -- -----	1,3,5-8, 18-23,35,37

INTERNATIONAL SEARCH REPORT

Information on patent family members

01/12/98

International application No.

PCT/NO 98/00185

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0721189 A2	10/07/96	JP 8185697 A	16/07/96
		US 5668753 A	16/09/97
EP 0395886 A2	07/11/90	JP 2262362 A	25/10/90
		JP 2262364 A	25/10/90
		JP 2310962 A	26/12/90
US 5592409 A	07/01/97	JP 7249288 A	26/09/95
		JP 8008408 A	12/01/96
		US 5633821 A	27/05/97